



TO-8000 Serie



1. Specification

Test Conditions: $T_A = +25 \pm 3 \text{ }^\circ\text{C}$; $V_S = V_{Snom}$; $V_C = V_{Ccenter}$

Type:	TO-8XYZ
Frequency range:	100.0 MHz ... 1500.0 MHz
Supply voltage V_S (nominal values $\pm 5\%$): +5.0 V: +3.3 V: +12 V:	X 1 2 3
Initial frequency tolerance (Note 1): 24 h after reflow ($T_{peak} = +260 \text{ }^\circ\text{C}$ for 10 sec max): Note 1: $V_C = +2.5 \text{ V}$ for X=1; $V_C = +1.65 \text{ V}$ for X=2; $V_C = +5.0 \text{ V}$ for X=3	$\leq \pm 1.0 \text{ ppm}$ $\leq \pm 1.5 \text{ ppm}$
Temperature range options: 0 $^\circ\text{C}$ to +50 $^\circ\text{C}$: -10 $^\circ\text{C}$ to +60 $^\circ\text{C}$: 0 $^\circ\text{C}$ to +70 $^\circ\text{C}$: -20 $^\circ\text{C}$ to +70 $^\circ\text{C}$: -30 $^\circ\text{C}$ to +85 $^\circ\text{C}$: -40 $^\circ\text{C}$ to +85 $^\circ\text{C}$:	Y 1 2 3 4 5 6
Frequency stability options: $\leq \pm 0.5 \text{ ppm}$: $\leq \pm 1.0 \text{ ppm}$: $\leq \pm 1.5 \text{ ppm}$: $\leq \pm 2.0 \text{ ppm}$:	Z 1 2 3 4
Frequency stability vs. supply voltage changes $V_S \pm 5\%$: vs. load changes $\pm 10\%$:	$\leq \pm 0.1 \text{ ppm}$ $\leq \pm 0.1 \text{ ppm}$
Aging @ +40 $^\circ\text{C}$: Low Aging option:	$\leq \pm 1.0 \text{ ppm / year}$ $\leq \pm 3.0 \text{ ppm / 10 years}$
Frequency Tuning Options : Fixed frequency oscillator: $\geq \pm 5 \text{ ppm}$: $\geq \pm 10 \text{ ppm}$: $\geq \pm 15 \text{ ppm}$:	X F E T
Control voltage range V_C : TO-61xx (+5 V): TO-62xx (+3.3 V): TO-63xx (+12 V):	+0.5 V to +4.5 V +0.3 V to +3.0 V +0.5 V to +9.5 V
Transfer function / Linearity:	positive / 10 %

4				KVG Quartz Crystal Technology GmbH P.O. Box 61 D-74924 Neckarbischofsheim Tel. +49 (0) 7263 / 648-0 Fax. +49 (0) 7263 / 6196
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1. Specification continued

Output signal Option H : (LV)HCMOS ($\leq 100\text{MHz}$): high level : low level : load : duty cycle:	$\text{low} \geq 90\% V_S$ $\text{high} \leq 10\% V_S$ $1 \text{ k}\Omega // 15 \text{ pF}$ $45 / 55 \%$		
Current consumption for (LV)HCMOS:	$< 40 \text{ mA}$		
Output signal Option P : LVPECL ($\leq 200\text{MHz}$): level: load : duty cycle :	$\text{High} \geq 2.275 \text{ V}, \text{ Low} \leq 1.68 \text{ V}$ 50 Ohm $45 / 55\%$		
Current consumption for LVPECL:	$< 100 \text{ mA}$		
Output signal Option L : LVDS ($\leq 200\text{MHz}$): level: load: duty cycle:	$247 \text{ mV} \leq V_{\text{OD}} \leq 454 \text{ mV}$ 100 Ohm sym. $45\% / 55\%$		
Current consumption for LVDS:	$\leq 40 \text{ mA}$		
Output signal Option S : Sinewave : Level: Load:	$\geq +3 \text{ dBm}$ 50 Ohm		
Current consumption for Sinewave:	$\leq 30 \text{ mA}$		
Harmonics:	$\leq -30 \text{ dBc}$		
Subharmonics ($f > 120 \text{ MHz}$) :	$\leq -40 \text{ dBc}$		
RMS Jitter (12 kHz to 20 MHz):	$\leq 0.1 \text{ ps}$		
Typical Phase Noise:	<u>100 MHz</u>	<u>500 MHz</u>	<u>1 GHz</u>
10 Hz:	$\leq -90 \text{ dBc/Hz}$	$\leq -70 \text{ dBc/Hz}$	$\leq -60 \text{ dBc/Hz}$
100 Hz:	$\leq -125 \text{ dBc/Hz}$	$\leq -110 \text{ dBc/Hz}$	$\leq -100 \text{ dBc/Hz}$
1 kHz:	$\leq -140 \text{ dBc/Hz}$	$\leq -130 \text{ dBc/Hz}$	$\leq -120 \text{ dBc/Hz}$
10 kHz: (1)	$\leq -160 \text{ dBc/Hz}$	$\leq -145 \text{ dBc/Hz}$	$\leq -140 \text{ dBc/Hz}$
100 kHz: (1)	$\leq -165 \text{ dBc/Hz}$	$\leq -150 \text{ dBc/Hz}$	$\leq -150 \text{ dBc/Hz}$
1 MHz: (1)	$\leq -165 \text{ dBc/Hz}$	$\leq -150 \text{ dBc/Hz}$	$\leq -150 \text{ dBc/Hz}$
Note (1): phase noise values are depending on supply voltage and output signal type			
Storage temperature range:	$-55 \text{ to } +105 \text{ }^\circ\text{C}$		

2. Marking

1. ww KVG yy
3. Specification / Part Number
2. Frequency

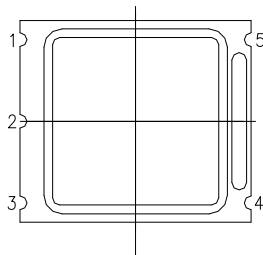
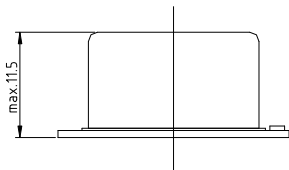
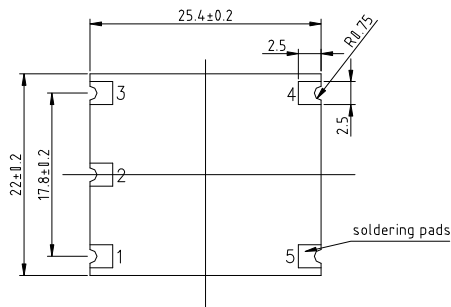
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3. Environmental conditions

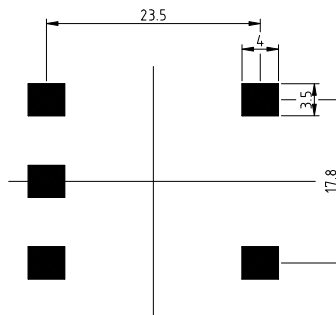
According to KVG Product Qualification Procedure AA-QM-202

4. Case

BF144-11.5-SMD



Foot print for PCB Design

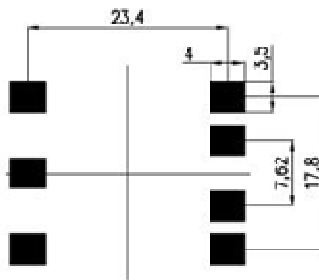
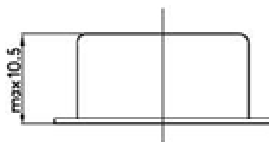
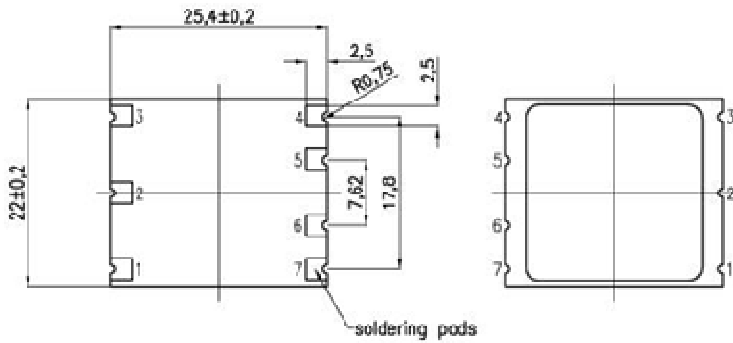


Pin configuration

1. Supply voltage V_S
2. RF output
3. Ground, case
4. Control voltage V_C or N.C.
5. Ground, case

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BF144-SMD-10.5 (for compl. output signals LVPECL, LVDS)



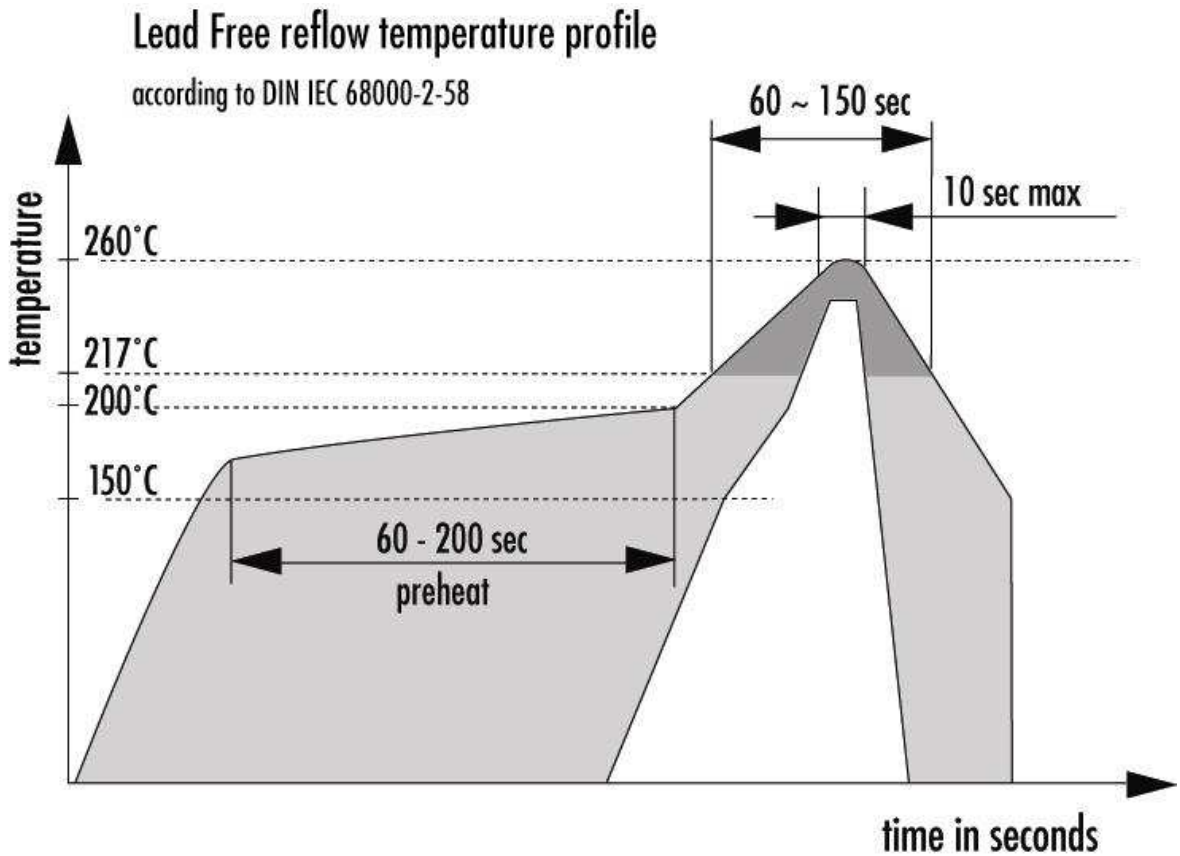
Footprint: for PCB design

Pin configuration

1. Control Voltage V_C or N.C.
2. N.C.
3. Supply voltage V_S
4. $V_{E/D}$ option
5. RF out 1
6. RF out 2 (complementary)
7. GND

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5. Reflow Soldering Profile



6. Ordering Information

Package Code	Supply Voltage	Temp. Range	Frequ. Stability	Frequ. Control	Output Signal	RoHS compl.	Nominal Frequency
BF144-SMD-10.5A	3.3 V	-20/+70 °C	±1 ppm	±5 ppm	Sine		100.000
TO-8	2	4	2	F	S	-LF	- XX.YYY MHz

Example: TO-8242FS-LF-100.000 MHz

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