



ROHS-Compliant Product

V-75000P/L Series



1. Specification		
Type: Fundamental mode VCXO	V-75A	V-75H
Frequency range:	1.5 MHz ... 200 MHz	65 MHz ... 200 MHz
Standard frequencies:	77.76, 106.25, 122.88, 125, 155.52, 156.25, 200 MHz	
Supply voltage V_S :	+3.3 V \pm 5 %	+2.5 V \pm 5 %
Current consumption @ $f_N = 1.5$ MHz ... 65 MHz, LVPECL: @ $f_N = 1.5$ MHz ... 65 MHz, LVDS: @ $f_N = 65$ MHz ... 200 MHz, LVPECL: @ $f_N = 65$ MHz ... 200 MHz, LVDS:	85 mA 45 mA 100 mA 80 mA	
Temperature range options -20 °C to +70 °C: -40 °C to +85 °C:	2070 4085	
Frequency stability options ⁽¹⁾ $\leq \pm 25$ ppm (n.a. for - 40 °C to + 85 °C): $\leq \pm 50$ ppm:	D F	
Control voltage range V_C :	+0.3 V to +3.0 V	+0.0 V to +2.5 V
Absolute pulling range (APR) referred to f @ $V_C = +1.65$ V / +1.25 V:	$\geq \pm 50$ ppm	
Modulation bandwidth (3 dB cut-off freq.):	≥ 25 kHz	
Control voltage input impedance:	≥ 50 kOhm	
Transfer function / linearity:	Positive / 10 %	
Output signal type options:		
P: LVPECL level ($V_S = +3.3$ V): level ($V_S = +2.5$ V): load: duty cycle: rise time, fall time 20 % / 80 %:	High: $\geq +2.275$ V, Low: $\leq +1.680$ V High: $\geq +1.475$ V, Low: $\leq +1.095$ V 50 Ohm 45 / 55 % ≤ 1.0 nsec	

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1		31.03.2015	Schweickert	
ED	Description	Date	Name	



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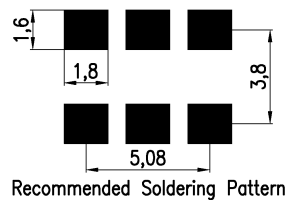
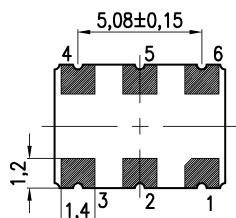
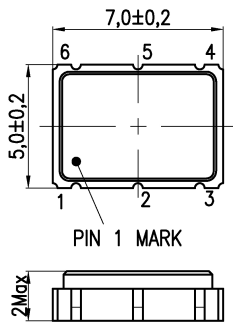


1. Specification (continued)	
L: LVDS level ($V_S = +3.3$ V): level ($V_S = +2.5$ V): load: duty cycle: rise time, fall time 20 % / 80 %:	High: $\leq +1.6$ V, Low: $\geq +0.9$ V High: $\leq +1.6$ V, Low: $\geq +0.9$ V 100 Ohm 40 / 60 % ≤ 1.0 nsec
Start-up time:	≤ 3 ms
RMS Phase Jitter (12 kHz - 20 MHz):	≤ 1 ps rms
Output Enable / Disable Enable: Disable:	Low ($\leq 30\% V_S$) or N.C. High ($\geq 70\% V_S$)
Storage temperature range:	-55 °C ... $+125$ °C
Note (1):	overall frequency stability includes: Initial tolerance, 1st year aging, frequency stability vs. temperature, vs. supply voltage changes $\pm 5\%$, vs. load changes $\pm 5\%$
2. Environmental conditions	
According to KVG Product Qualification Procedure AA-QM-200	
3. Marking	
Manufacturer's name, date code (week/year) Specification Center frequency	

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4. Case

Case style: BF-189-2.0B



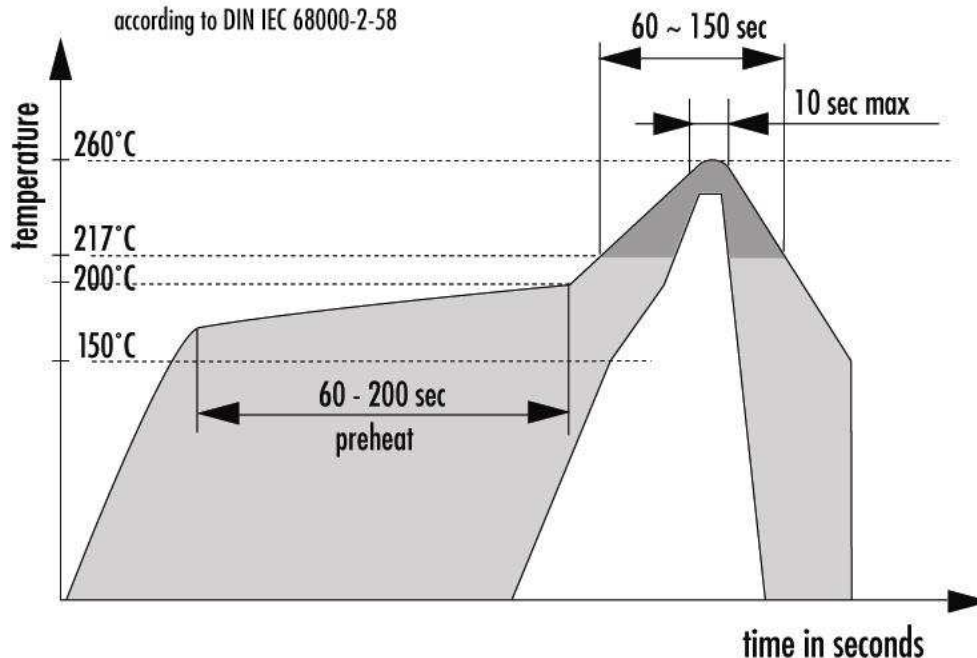
Pin configuration

1. Control voltage V_C
2. E/D control input
3. Ground, Case
4. RF output
5. Compl. RF output
6. Supply voltage V_S

5. Reflow soldering profile

Lead Free reflow temperature profile

according to DIN IEC 68000-2-58



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6. Ordering Information

Type & Package code	Supply voltage	Temperature range LOW/HIGH	Frequency stability	Output voltage	RoHS compl.	Nominal frequency
V-75: BF-189-2.0B	A: 3.3 V H: 2.5 V	2070: -20 / +70 °C 4085: -40 / +85 °C	D: ± 25 ppm F: ± 50 ppm	P: LVPECL L: LVDS	-LF	- XX.YYY MHz

Example: V-75A2070FP-LF-150.000MHz

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